

Day : Wednesday

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## Inventor Name Search Result

Your Search was:

Last Name = OLSZEWSKI

First Name = BRET

| Application#    | Patent#    | Status | Date Filed | Title                                                                                                                                                                                                                         | Inventor Name          |
|-----------------|------------|--------|------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|------------------------|
| <u>11077324</u> | Not Issued | 020    | 03/10/2005 | SYSTEM AND METHOD OF MAINTAINING STRICT HARDWARE AFFINITY IN A VIRTUALIZED LOGICAL PARTITIONED (LPAR) MULTIPROCESSOR SYSTEM WHILE ALLOWING ONE PROCESSOR TO DONATE EXCESS PROCESSOR CYCLES TO OTHER PARTITIONS WHEN WARRANTED | OLSZEWSKI, BRET RONALD |
| <u>11011248</u> | Not Issued | 020    | 12/14/2004 | SCHEDULING THREADS IN A MULTI-THREADED COMPUTER                                                                                                                                                                               | OLSZEWSKI, BRET RONALD |
| <u>10955182</u> | Not Issued | 030    | 09/30/2004 | METHOD AND APPARATUS FOR MONITORING VARIABLE SPEED MICROPROCESSOR PERFORMANCE CAUSED BY POWER MANAGEMENT                                                                                                                      | OLSZEWSKI, BRET RONALD |
| <u>10932702</u> | Not Issued | 020    | 09/02/2004 | METHOD, APPARATUS, AND COMPUTER PROGRAM PRODUCT FOR PROVIDING A SELF-TUNABLE PARAMETER USED FOR DYNAMICALLY YIELDING AN IDLE PROCESSOR                                                                                        | OLSZEWSKI, BRET RONALD |
| <u>10897329</u> | Not Issued | 020    | 07/22/2004 | METHOD AND APPARATUS FOR SUPPORTING SHARED LIBRARY TEXT REPLICATION ACROSS A FORK SYSTEM CALL                                                                                                                                 | OLSZEWSKI, BRET RONALD |
| <u>10855847</u> | Not Issued | 030    | 05/27/2004 | SYSTEM AND METHOD FOR IMPROVING PERFORMANCE OF DYNAMIC MEMORY REMOVALS BY REDUCING FILE CACHE SIZE                                                                                                                            | OLSZEWSKI, BRET RONALD |
| <u>10787659</u> | Not Issued | 030    | 02/26/2004 | MEASURING PROCESSOR USE IN A HARDWARE MULTITHREADING PROCESSOR ENVIRONMENT                                                                                                                                                    | OLSZEWSKI, BRET RONALD |
| <u>10342061</u> | Not        | 168    | 01/14/2003 | APPARATUS AND METHOD FOR                                                                                                                                                                                                      | OLSZEWSKI, BRET        |

|                 |                |     |            |                                                                                                                                              |                           |
|-----------------|----------------|-----|------------|----------------------------------------------------------------------------------------------------------------------------------------------|---------------------------|
|                 | Issued         |     |            | DISPATCHING FIXED PRIORITY<br>THREADS USING A GLOBAL<br>RUN QUEUE IN A MULTIPLE<br>RUN QUEUE SYSTEM                                          | RONALD                    |
| <u>10341996</u> | Not<br>Issued  | 168 | 01/14/2003 | APPARATUS AND METHOD FOR<br>DISPATCHING FIXED PRIORITY<br>THREADS USING A GLOBAL<br>RUN QUEUE IN A MULTIPLE<br>RUN QUEUE SYSTEM              | OLSZEWSKI, BRET<br>RONALD |
| <u>10341876</u> | Not<br>Issued  | 168 | 01/14/2003 | APPARATUS AND METHOD FOR<br>DISPATCHING FIXED PRIORITY<br>THREADS USING A GLOBAL<br>RUN QUEUE IN A MULTIPLE<br>RUN QUEUE SYSTEM              | OLSZEWSKI, BRET<br>RONALD |
| <u>09870609</u> | Not<br>Issued  | 071 | 05/31/2001 | RUN QUEUE OPTIMIZATION<br>WITH HARDWARE<br>MULTITHREADING FOR<br>AFFINITY                                                                    | OLSZEWSKI, BRET<br>RONALD |
| <u>09779369</u> | <u>6845504</u> | 150 | 02/08/2001 | METHOD AND SYSTEM FOR<br>MANAGING LOCK CONTENTION<br>IN A COMPUTER SYSTEM                                                                    | OLSZEWSKI, BRET R.        |
| <u>09513819</u> | <u>6598143</u> | 150 | 02/24/2000 | METHOD TO INCREASE<br>PERFORMANCE OF ACQUIRING<br>FREE MEMORY PAGES                                                                          | OLSZEWSKI, BRET<br>RONALD |
| <u>09506230</u> | Not<br>Issued  | 168 | 02/17/2000 | APPARATUS AND METHOD FOR<br>DISPATCHING FIXED PRIORITY<br>THREADS USING A GLOBAL<br>RUN QUEUE IN A MULTIPLE<br>RUN QUEUE SYSTEM              | OLSZEWSKI, BRET<br>RONALD |
| <u>09339711</u> | <u>6339818</u> | 150 | 06/24/1999 | METHOD AND SYSTEM FOR<br>DYNAMICALLY LOCATING<br>FREQUENTLY ACCESSED<br>MEMORY REGIONS OR<br>LOCATIONS                                       | OLSZEWSKI, BRET<br>RONALD |
| <u>08706053</u> | <u>5809548</u> | 250 | 08/30/1996 | SYSTEM AND METHOD FOR<br>ZEROING PAGES WITH CACHE<br>LINE INVALIDATE<br>INSTRUCTIONS IN AN LRU<br>SYSTEM HAVING DATE CACHE<br>WITH TIME TAGS | OLSZEWSKI, BRET R.        |
| <u>08538071</u> | <u>6708296</u> | 150 | 10/02/1995 | METHOD AND SYSTEM FOR<br>SELECTING AND<br>DISTINGUISHING AN EVENT<br>SEQUENCE USING AN<br>EFFECTIVE ADDRESS IN A<br>PROCESSING SYSTEM        | OLSZEWSKI, BRET R.        |

Inventor Search Completed: No Records to Display.

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**Inventor Name Search Result**

Your Search was:

Last Name = SRINIVAS

First Name = MYSORE

| Application#    | Patent#    | Status | Date Filed | Title                                                                                                                                                                                                                         | Inventor Name                   |
|-----------------|------------|--------|------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------|
| <u>11152557</u> | Not Issued | 030    | 06/14/2005 | WEIGHTED LRU FOR ASSOCIATIVE CACHES                                                                                                                                                                                           | SRINIVAS, MYSORE SATHYANARAYANA |
| <u>11077324</u> | Not Issued | 020    | 03/10/2005 | SYSTEM AND METHOD OF MAINTAINING STRICT HARDWARE AFFINITY IN A VIRTUALIZED LOGICAL PARTITIONED (LPAR) MULTIPROCESSOR SYSTEM WHILE ALLOWING ONE PROCESSOR TO DONATE EXCESS PROCESSOR CYCLES TO OTHER PARTITIONS WHEN WARRANTED | SRINIVAS, MYSORE SATHYANARAYANA |
| <u>11006371</u> | Not Issued | 030    | 12/07/2004 | EFFICIENT MEMORY UPDATE PROCESS FOR ON-THE-FLY INSTRUCTION TRANSLATION FOR WELL BEHAVED APPLICATIONS EXECUTING ON A WEAKLY-ORDERED PROCESSOR                                                                                  | SRINIVAS, MYSORE SATHYANARAYANA |
| <u>10932723</u> | Not Issued | 030    | 09/02/2004 | METHOD AND APPARATUS FOR ENABLING EARLY MEMORY READS FOR NODE LOCAL DATA                                                                                                                                                      | SRINIVAS, MYSORE SATHYANARAYANA |
| <u>10916976</u> | Not Issued | 020    | 08/12/2004 | SCHEDULING THREADS IN A MULTIPROCESSOR COMPUTER                                                                                                                                                                               | SRINIVAS, MYSORE SATHYANARAYANA |
| <u>10870458</u> | Not Issued | 030    | 06/17/2004 | SYSTEM AND METHOD FOR AN OPTIMIZED LEAST RECENTLY USED LOOKUP CACHE                                                                                                                                                           | SRINIVAS, MYSORE SATHYANARAYANA |
| <u>10845549</u> | Not Issued | 030    | 05/13/2004 | DYNAMIC MEMORY MANAGEMENT OF UNALLOCATED MEMORY IN A LOGICAL PARTITIONED DATA PROCESSING SYSTEM                                                                                                                               | SRINIVAS, MYSORE SATHYANARAYANA |
| <u>10828455</u> | Not Issued | 030    | 04/20/2004 | SYSTEM AND METHOD FOR DYNAMICALLY ADJUSTING                                                                                                                                                                                   | SRINIVAS, MYSORE SATHYANARAYANA |

|                 |                |     |            |                                                                                                                                    |                                    |
|-----------------|----------------|-----|------------|------------------------------------------------------------------------------------------------------------------------------------|------------------------------------|
|                 |                |     |            | READ AHEAD VALUES BASED<br>UPON MEMORY USAGE                                                                                       |                                    |
| <u>10787659</u> | Not<br>Issued  | 030 | 02/26/2004 | MEASURING PROCESSOR USE<br>IN A HARDWARE<br>MULTITHREADING<br>PROCESSOR ENVIRONMENT                                                | SRINIVAS, MYSORE<br>SATHYANARAYANA |
| <u>10671132</u> | Not<br>Issued  | 020 | 09/25/2003 | SYSTEM AND METHOD FOR<br>CPI SCHEDULING ON SMT<br>PROCESSORS                                                                       | SRINIVAS, MYSORE<br>SATHYANARAYANA |
| <u>10671057</u> | Not<br>Issued  | 030 | 09/25/2003 | SYSTEM AND METHOD FOR<br>CPI LOAD BALANCING IN SMT<br>PROCESSORS                                                                   | SRINIVAS, MYSORE<br>SATHYANARAYANA |
| <u>10342061</u> | Not<br>Issued  | 168 | 01/14/2003 | APPARATUS AND METHOD<br>FOR DISPATCHING FIXED<br>PRIORITY THREADS USING A<br>GLOBAL RUN QUEUE IN A<br>MULTIPLE RUN QUEUE<br>SYSTEM | SRINIVAS, MYSORE<br>SATHYANARAYANA |
| <u>10341996</u> | Not<br>Issued  | 168 | 01/14/2003 | APPARATUS AND METHOD<br>FOR DISPATCHING FIXED<br>PRIORITY THREADS USING A<br>GLOBAL RUN QUEUE IN A<br>MULTIPLE RUN QUEUE<br>SYSTEM | SRINIVAS, MYSORE<br>SATHYANARAYANA |
| <u>10341876</u> | Not<br>Issued  | 168 | 01/14/2003 | APPARATUS AND METHOD<br>FOR DISPATCHING FIXED<br>PRIORITY THREADS USING A<br>GLOBAL RUN QUEUE IN A<br>MULTIPLE RUN QUEUE<br>SYSTEM | SRINIVAS, MYSORE<br>SATHYANARAYANA |
| <u>10138890</u> | Not<br>Issued  | 030 | 05/02/2002 | SYSTEM AND METHOD FOR<br>THREAD SCHEDULING WITH<br>WEAK PREEMPTION POLICY                                                          | SRINIVAS, MYSORE<br>SATHYANARAYANA |
| <u>09870609</u> | Not<br>Issued  | 071 | 05/31/2001 | RUN QUEUE OPTIMIZATION<br>WITH HARDWARE<br>MULTITHREADING FOR<br>AFFINITY                                                          | SRINIVAS, MYSORE<br>SATHYANARAYANA |
| <u>09779369</u> | <u>6845504</u> | 150 | 02/08/2001 | METHOD AND SYSTEM FOR<br>MANAGING LOCK<br>CONTENTION IN A COMPUTER<br>SYSTEM                                                       | SRINIVAS, MYSORE S.                |
| <u>09627226</u> | <u>6711644</u> | 150 | 07/27/2000 | APPARATUS AND METHOD<br>FOR COMMUNICATION THE<br>COMPLETION OF<br>ASYNCHRONOUS<br>INPUT/OUTPUT                                     | SRINIVAS, MYSORE<br>SATHYANARAYA   |
| <u>09615768</u> | <u>6735769</u> | 150 | 07/13/2000 | APPARATUS AND METHOD<br>FOR INITIAL LOAD<br>BALANCING IN A MULTIPLE                                                                | SRINIVAS, MYSORE<br>SATHYANARAYANA |

|                 |                |     |            |                                                                                                                                    |                                    |
|-----------------|----------------|-----|------------|------------------------------------------------------------------------------------------------------------------------------------|------------------------------------|
|                 |                |     |            | RUN QUEUE SYSTEM                                                                                                                   |                                    |
| <u>09513810</u> | <u>6651146</u> | 150 | 02/24/2000 | METHOD AND APPARATUS<br>FOR MANAGING ACCESS<br>CONTENTION TO A LINEAR<br>LIST WITHOUT THE USE OF<br>LOCKS                          | SRINIVAS, MYSORE<br>SATHYANARAYANA |
| <u>09506230</u> | Not<br>Issued  | 168 | 02/17/2000 | APPARATUS AND METHOD<br>FOR DISPATCHING FIXED<br>PRIORITY THREADS USING A<br>GLOBAL RUN QUEUE IN A<br>MULTIPLE RUN QUEUE<br>SYSTEM | SRINIVAS, MYSORE<br>SATHYANARAYANA |

Inventor Search Completed: No Records to Display.

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**Inventor Name Search Result**

Your Search was:

Last Name = ROMERO

First Name = LILIAN

| Application#    | Patent#       | Status | Date Filed | Title                                                                     | Inventor Name     |
|-----------------|---------------|--------|------------|---------------------------------------------------------------------------|-------------------|
| <u>09870609</u> | Not<br>Issued | 071    | 05/31/2001 | RUN QUEUE OPTIMIZATION<br>WITH HARDWARE<br>MULTITHREADING FOR<br>AFFINITY | ROMERO, LILIAN R. |

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